查询TODV 640 ---> 1240供应商

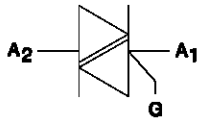
SGS-THOMSON MICROELECTRONICS

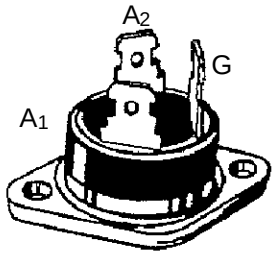
TODV 640 ---> 1240

ALTERNISTORS

FEATURES

- HIGH COMMUTATION : > 142 A/ms (400Hz)
- INSULATING VOLTAGE = 2500V(RMS) (UL RECOGNIZED : EB1734)
- HIGH VOLTAGE CAPABILITY : V_{DRM} = 1200 V





RD91
(Plastic)

DESCRIPTION

The TODV 640 ---> 1240 use a high performance passivated glass alternistor technology. Featuring very high commutation levels and high surge current capability, this family is well adapted to power control on inductive load (motor, transformer...)

ABSOLUTE RATINGS (limiting values)

Symbol	Parameter		Value	Unit
$I_{T(RMS)}$	RMS on-state current (360° conduction angle)	Tc = 75 °C	40	A
I_{TSM}	Non repetitive surge peak on-state current (Tj initial = 25°C)	tp = 2.5 ms	590	A
		tp = 8.3 ms	370	
		tp = 10 ms	350	
I^2t	I^2t value	tp = 10 ms	610	A^2s
dI/dt	Critical rate of rise of on-state current Gate supply : I_G = 500mA di_G/dt = 1A/µs	Repetitive F = 50 Hz	20	A/µs
		Non Repetitive	100	
Tstg Tj	Storage and operating junction temperature range		- 40 to + 150 - 40 to + 125	°C °C
Tl	Maximum lead temperature for soldering during 10 s at 4.5 mm from case		260	°C

Symbol	Parameter	TODV				Unit
		640	840	1040	1240	
V_{DRM} V_{RRM}	Repetitive peak off-state voltage Tj = 125 °C	600	800	1000	1200	V

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
Rth (c-h)	Contact (case-heatsink) with grease	0.1	°C/W
Rth (j-c) DC	Junction to case for DC	1.2	°C/W
Rth (j-c) AC	Junction to case for 360° conduction angle (F= 50 Hz)	0.9	°C/W

GATE CHARACTERISTICS (maximum values)

$P_{G\ (AV)}$ = 1W P_{GM} = 40W (tp = 20 µs) I_{GM} = 8A (tp = 20 µs) V_{GM} = 16V (tp = 20 µs).

ELECTRICAL CHARACTERISTICS

Symbol	Test Conditions		Quadrant		Value	Unit
I_{GT}	V_D=12V (DC) R_L=33Ω	Tj=25°C	I-II-III	MAX	200	mA
V_{GT}	V_D=12V (DC) R_L=33Ω	Tj=25°C	I-II-III	MAX	1.5	V
V_{GD}	V_D=V_{DRM} R_L=3.3kΩ	Tj=125°C	I-II-III	MIN	0.2	V
tgt	V_D=V_{DRM} I_G = 500mA dI_G/dt = 3A/µs	Tj=25°C	I-II-III	TYP	2.5	µs
I_L	I_G=1.2 I_{GT}	Tj=25°C	I-III	TYP	100	mA
			II		200	
I_H *	I_T= 500mA gate open	Tj=25°C		TYP	50	mA
V_{TM} *	I_{TM}= 60A tp= 380µs	Tj=25°C		MAX	1.8	V
I_{DRM} I_{RRM}	V_{DRM} Rated V_{RRM} Rated	Tj=25°C		MAX	0.02	mA
		Tj=125°C		MAX	8	
dV/dt *	Linear slope up to V_D=67%V_{DRM} gate open	Tj=125°C		MIN	500	V/µs
(dI/dt)c *	(dV/dt)c = 200V/µs	Tj=125°C		MIN	35	A/ms
	(dV/dt)c = 10V/µs				142	

* For either polarity of electrode A_2 voltage with reference to electrode A_1.

Fig.1 : Maximum RMS power dissipation versus RMS on-state current (F=50Hz).
(Curves are cut off by (dI/dt)c limitation)

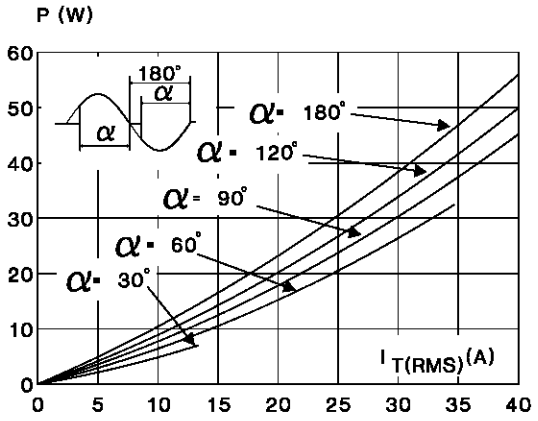


Fig.2 : Correlation between maximum RMS power dissipation and maximum allowable temperatures (T_{amb} and T_{case}) for different thermal resistances heatsink + contact.

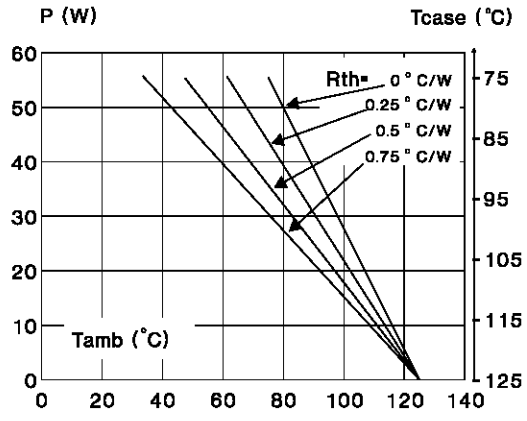


Fig.3 : RMS on-state current versus case temperature.

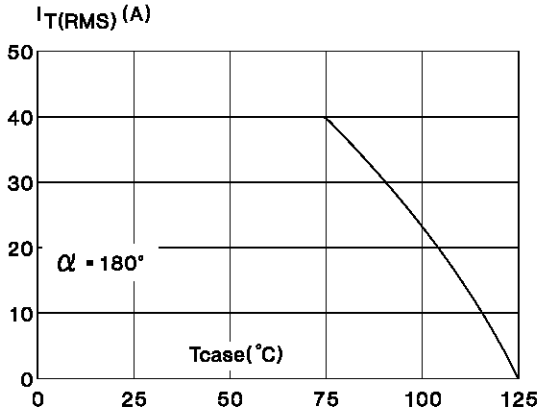


Fig.4 : Relative variation of thermal impedance junction to case versus pulse duration.

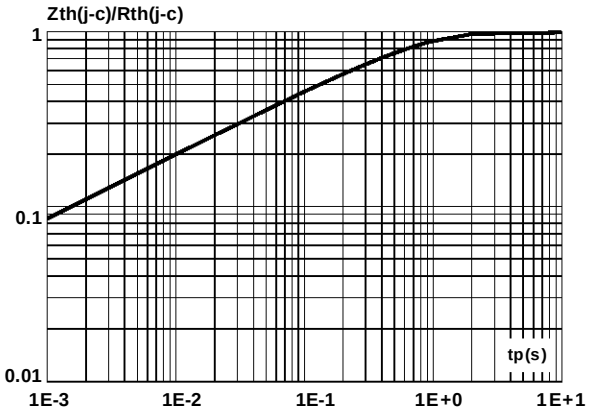


Fig.5 : Relative variation of gate trigger current and holding current versus junction temperature.

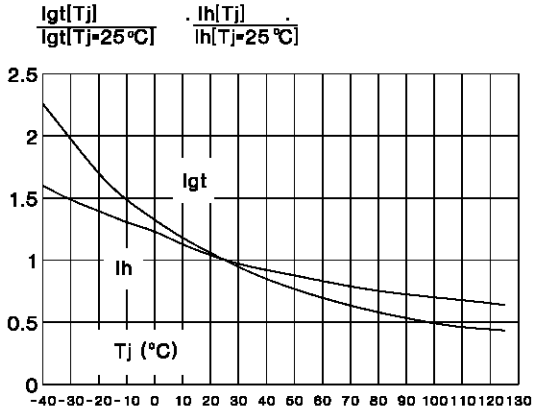


Fig.6 : Non Repetitive surge peak on-state current versus number of cycles.

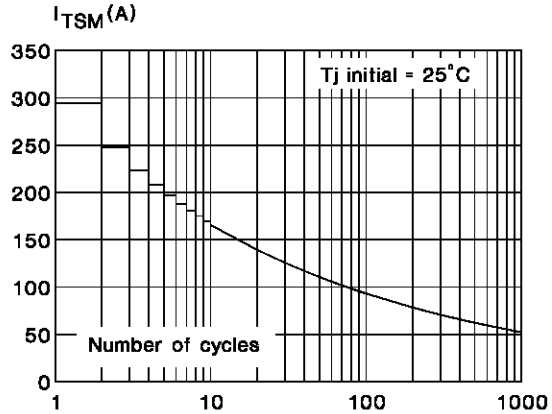


Fig.7 : Non repetitive surge peak on-state current for a sinusoidal pulse with width : t ≤ 10ms, and corresponding value of I^2t.

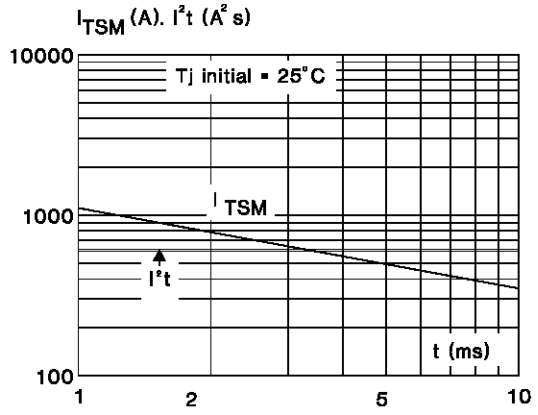


Fig.8 : On-state characteristics (maximum values).

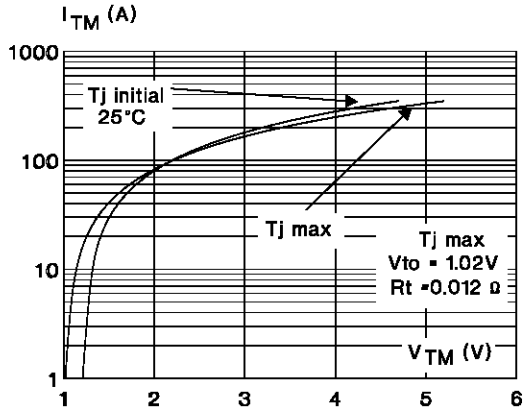
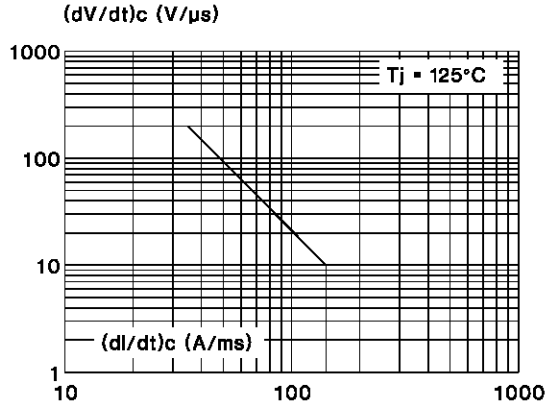
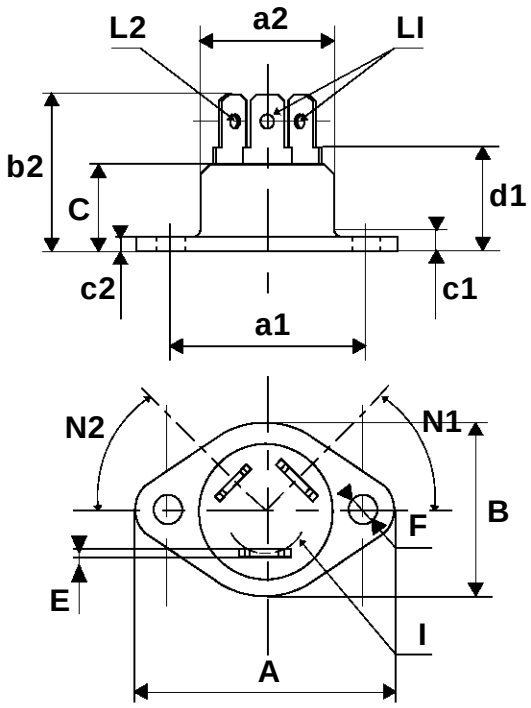


Fig.9 : Safe operating area.



PACKAGE MECHANICAL DATA

RD91 Plastic



REF.	DIMENSIONS			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A		40.00		1.575
a1	29.90	30.30	1.177	1.193
a2		22.00		0.867
B		27.00		1.063
b1	13.50	16.50	0.531	0.650
b2		24.00		0.945
C		14.00		0.551
c1		3.50		0.138
c2	1.95	3.00	0.077	0.118
E	0.70	0.90	0.027	0.035
F	4.00	4.50	0.157	0.177
I	11.20	13.60	0.441	0.535
L1	3.10	3.50	0.122	0.138
L2	1.70	1.90	0.067	0.075
N1	33°	43°	33°	43°
N2	28°	38°	28°	38°

Marking : type number
Weight : 20 g

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